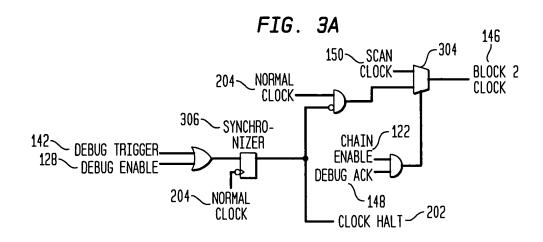


FIG. 2



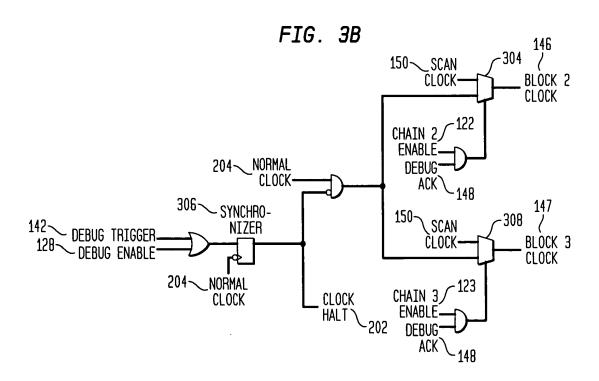


FIG. 4

202 CLOCK 1 HALT SYNCHROCLOCK 2 HALT DEBUG
CLOCK 3 HALT SYNCHRONIZER DEBUG
ENABLE 128

FIG. 5

